

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Patent Application of** )  
SEKIGUCHI et al. )  
**Application Number: To be Assigned** )  
**Filed: Concurrently herewith** )      **Art Unit 2818**  
**For: SEMICONDUCTOR MEMORY** )  
**Attorney Docket No. HITA.0480** )

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**INFORMATION DISCLOSURE STATEMENT**

The above-referenced application is a Continuation of U.S. Serial No. 10/287,678 filed November 5, 2002. It includes the same disclosure as U.S. patent application Serial No. 10/287,678.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

This Information Disclosure Statement is submitted with the above-captioned U.S. Continuation application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

Stanley P. Fisher  
Registration Number 24,344

Juan Carlos A. Marquez  
Registration Number 34,072

**REED SMITH LLP**  
3110 Fairview Park Drive  
Suite 1400  
Falls Church, Virginia 22042  
(703) 641-4200

**January 21, 2004**

<p><b>Form PTO 1449</b></p> <p>U.S. Department of commerce Patent and Trademark Office</p> <p>Information Disclosure Statement by Applicant</p>	<b>ATTY. DOCKET NUMBER</b>	<b>SERIAL NUMBER</b>
	<b>HITA.0470</b>	<b>To be assigned</b>
	<b>APPLICANT</b>	
	<b>SEKIGUCHI et al.</b>	
<b>FILING DATE</b>	<b>GROUP</b>	
<b>Concurrently herewith</b>		

## U.S. Patent Documents

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
XX	6,075,735	06/13/2000	Sugabayashi			10/01/1998
X	5,050,126	09/17/1991	Tanaka et al.			10/03/1988
X	5,371,711	12/06/1994	Nakayama, T.			03/03/1994
X	6,414,893	07/02/2002	Miyamoto, J.			08/19/1998
X	5,592,407	01/07/1997	Konishi et al.			02/23/1995

## Foreign Patent Documents

**Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)**

	XX	Satoshi Utsugi, Masami Hanyu, Yoshinori Muramatsu and Tadahiko Sugibayashi, "Noncomplimentary Rewriting and Serial-Data Coding Scheme for Shared-Sense-Amplifier Open-Bit-Line DRAM", IEEE Journal of Solid-State Circuits, Vol. 34, No. 10, October 1999, pp. 1391-1394

EXAMINER	DATE CONSIDERED
----------	-----------------

*Examiner: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant*

**X Cited by Examiner in Parent  
XX Filed by applicant in parent**